

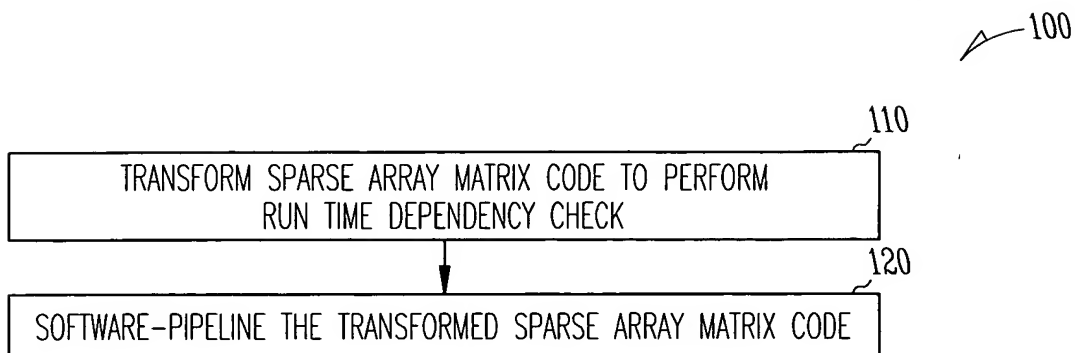


TITLE: SYSTEM AND METHOD FOR SOFTWARE-PIPELINING OF LOOPS WITH SPARSE MATRIX ROUTINES

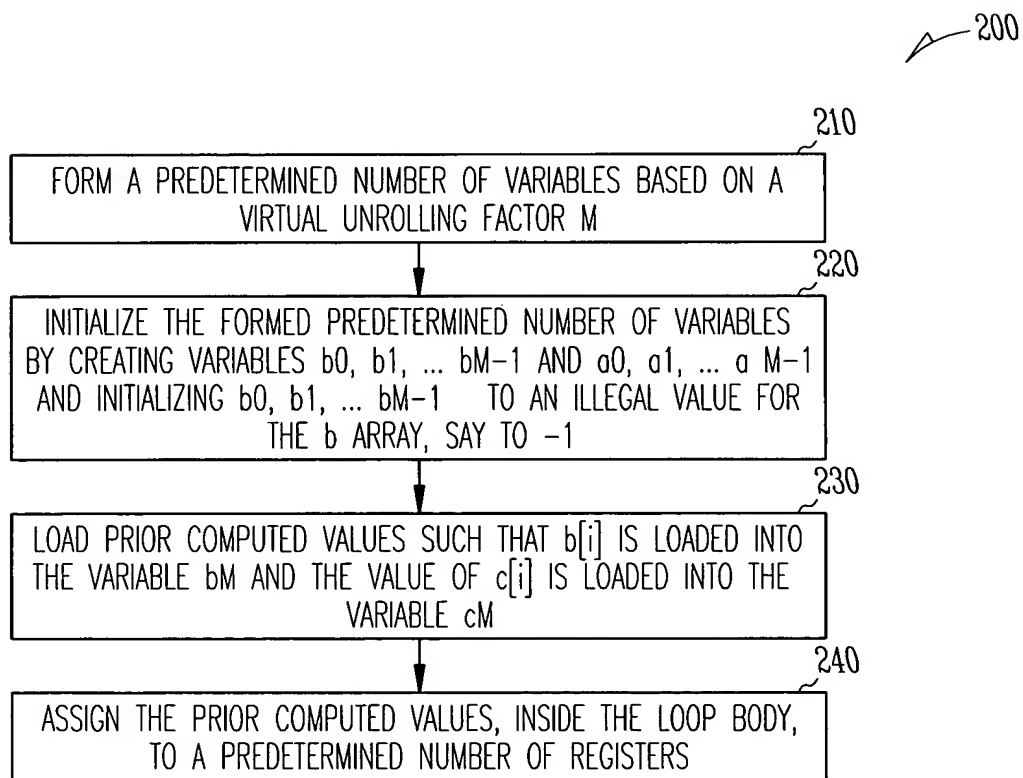
INVENTORS NAME: Kalyan Muthukumar et al.

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*Fig. 1*



*Fig. 2*



310

UNPIPELINED (17 CYCLES PER ITERATION)

TIME - > 0123456789012345678901234567890123456788901234567890123456789

ITER01: L-----A-----S

ITER02: L-----A-----S

ITER03: L-----A-----S

ITER04: L-----S

320

UNROLLED (9 CYLES PER ITERATION)

TIME > 0123456789012345678901234567890123456788901234567890123456789

ITER01: L-----A-----S

ITER02: L-----A-----S

ITER03: L-----A-----S

ITER04: L-----A-----S

ITER05: L-----A-----S

ITER06: L-----A-----S

330

SOFTWARE PIPELINED (5 CYLES PER ITERATION)

TIME-> 0123456789012345678901234567890123456788901234567890123456789

ITER01: L-----A-----S

ITER02: L-----A-----S

ITER03: L-----A-----S

ITER04: L-----A-----S

ITER05: L-----A-----S

ITER06: L-----A-----S

*Fig. 3*

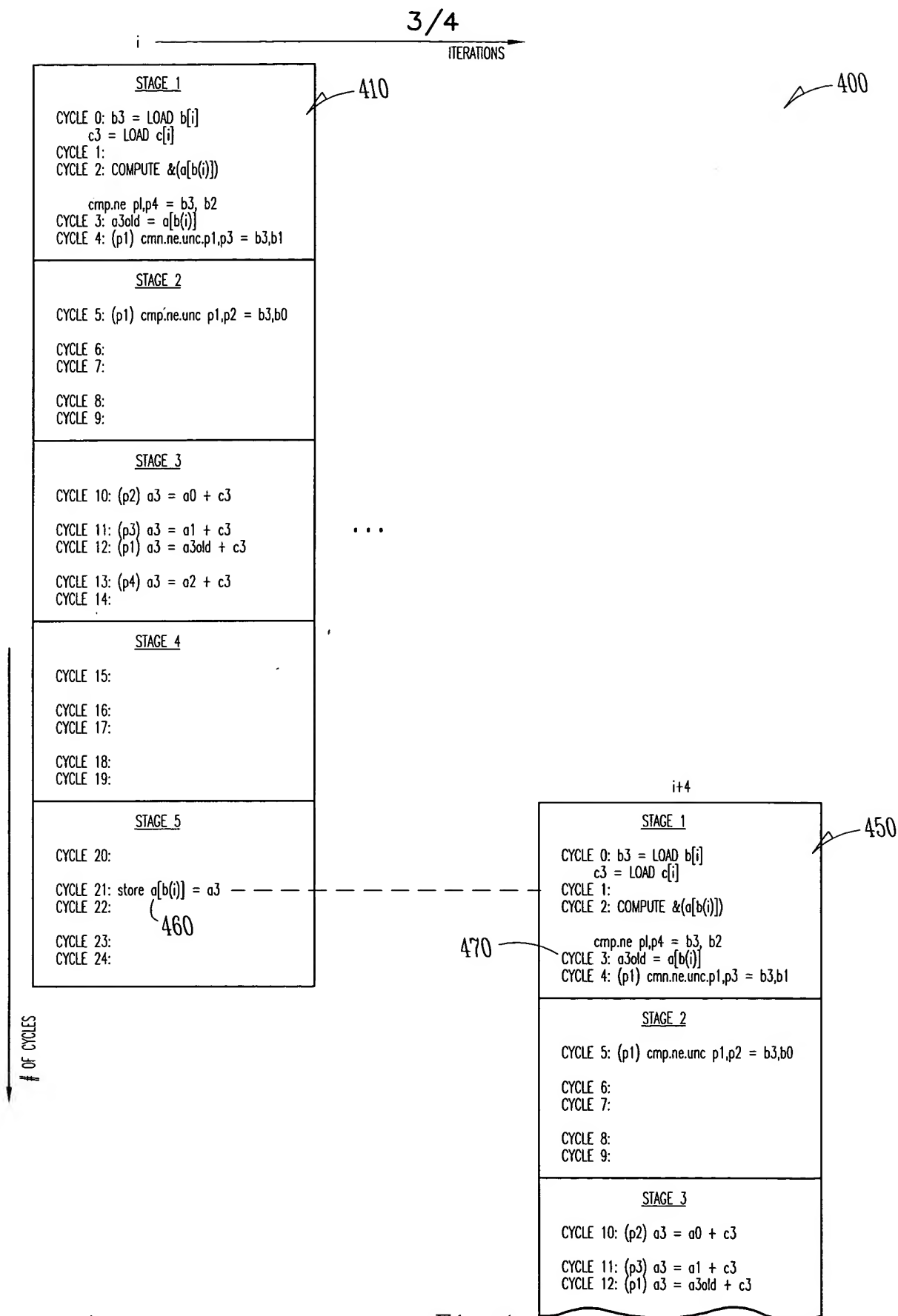


Fig. 4

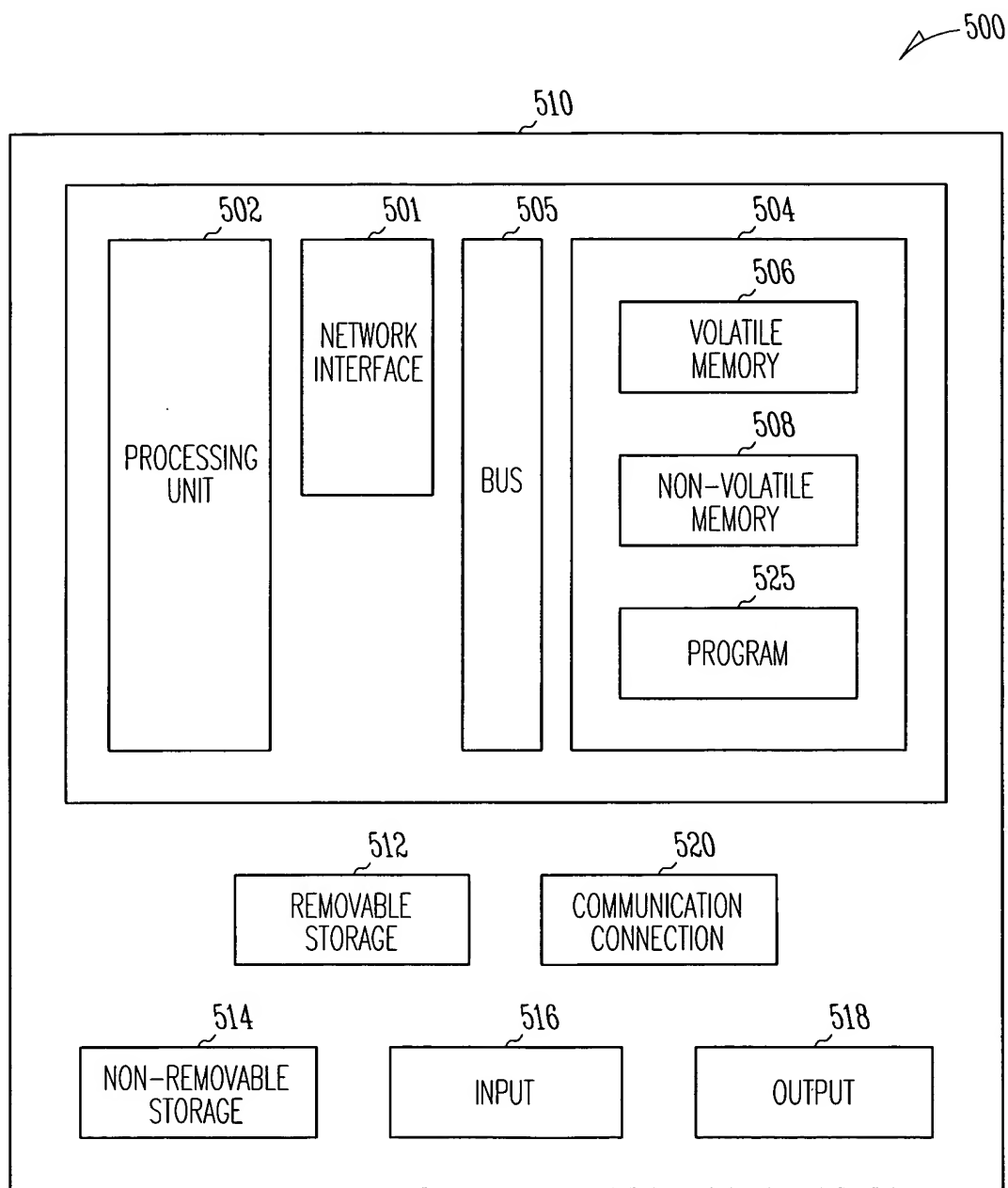


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*Fig. 5*